

**REMARKS**

At the outset, the Examiner is thanked for the thorough review and consideration of the pending application. The final Office Action dated June 27, 2006 and the Advisory Action dated October 25, 2006 have been received and their contents carefully reviewed.

Claims 1-5 are rejected by the Examiner. Claims 1 and 5 have been amended. No new matter has been added. Claims 1-22 remain pending in this application with claims 6-22 having been withdrawn.

In the Office Action, claim 5 is rejected under 35 U.S.C. § 102(e) as being anticipated by U.S. Patent No. 4,969,718 to Noguchi et al. (hereinafter “Noguchi”). Claims 1-4 are rejected under 35 U.S.C. § 103(a) as being unpatentable over Noguchi in view of U.S. Patent No. 6,327,008 to Fujiyoshi (hereinafter “Fujiyoshi”).

The rejection to claim 5 under 35 U.S.C. § 102(e) as being anticipated by Noguchi is respectfully traversed and reconsideration is requested. Applicant submits that Noguchi does not disclose each and every element of the claims.

Claim 5 recites a liquid crystal display device having a combination of features including “a first pixel electrode adjacent to each of the first and second data lines and spaced at a predetermined distance from the first data line; and a second pixel electrode adjacent to each of the second and third data lines and spaced apart from the second data line by a distance different from said distance between the first data line and the first pixel electrode, wherein the second pixel electrode has a larger dimension than the first pixel electrode, and wherein a parasitic capacitance between the second pixel electrode and the second data line is at least three times greater than a parasitic capacitance between the first pixel electrode and the first data line.”

The Examiner in rejecting claim 5 cites FIG 5A of Noguchi. In particular, the Examiner identifies elements 513 and 514 of FIG. 5A as the first and second data lines and identifies elements 522 and 517 as the first and second pixel electrodes, respectively. Applicant submits that the elements identified by the Examiner are not arranged as recited in claim 5. For example, element 522 is not “adjacent to each of the second and third data lines.” Accordingly, Applicant submits that Noguchi does not anticipate claim 5.

The rejection to claims 1-4 under 35 U.S.C. § 103(a) as being unpatentable over Noguchi in view of Fujiyoshi is respectfully traversed and reconsideration is requested.

Applicant submits that the cited references including Noguchi and Fujiyoshi, analyzed singly or in combination, do not teach or suggest each and every element of the claims.

Claims 1-4 each recite a liquid crystal display device having a combination of features including “a first pixel electrode adjacent to each of the first and second data lines within a first pixel and spaced at a predetermined distance from the first data line; and a second pixel electrode adjacent to each of the second and third data lines within a second pixel and spaced apart from the second data line by a distance different from said distance between the first data line and the first pixel electrode, wherein a voltage deviation in the first pixel due to parasitic capacitance is substantially the same as a voltage deviation in the second pixel due to parasitic capacitance.”

The Examiner in rejecting claim 5 cites FIG 5A of Noguchi identifying elements 513 and 514 as the first and second data lines, and elements 522 and 517 as the first and second pixel electrodes, respectively. Applicant submits that the elements identified by the Examiner are not taught or suggested by Noguchi to be as recited in claim 5. For example, element 522 is not “adjacent to each of the second and third data lines.” Accordingly, Applicant submits that Noguchi does not teach or suggest at least the above-identified elements of claim 4.

The Examiner cites Fujiyoshi to cure the deficiencies in the teaching of Noguchi. Applicant does not reach the Examiner’s conclusion regarding the teachings of Noguchi. Applicant submits that Noguchi and Fujiyoshi, analyzed separately or in any combination, do not teach at least “a first pixel electrode adjacent to the first and second data lines within a first pixel and spaced at a predetermined distance from the first data line; and a second pixel electrode adjacent to the second and third data lines within a second pixel and spaced apart from the second data line by a distance different from said distance between the first data line and the first pixel electrode, wherein a voltage deviation in the first pixel due to parasitic capacitance is substantially the same as a voltage deviation in the second pixel due to parasitic capacitance,” as recited in claim 1-4. Accordingly, Applicant submits that claims 1-4 are allowable over Noguchi and Fujiyoshi for at least this reason.

Applicant believes the application is in condition for allowance and early, favorable action is respectfully solicited.

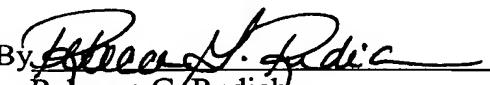
If for any reason the Examiner finds the application other than in condition for allowance, the Examiner is requested to call the undersigned attorney at (202) 496-7500 to discuss the steps

necessary for placing the application in condition for allowance. All correspondence should continue to be sent to the below-listed address.

If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. § 1.136, and any additional fees required under 37 C.F.R. § 1.136 for any necessary extension of time, or any other fees required to complete the filing of this response, may be charged to Deposit Account No. 50-0911. Please credit any overpayment to deposit Account No. 50-0911. *A duplicate copy of this sheet is enclosed.*

Respectfully submitted,

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By:   
Rebecca G. Rudich  
Registration No. 41,786  
McKENNA LONG & ALDRIDGE LLP  
1900 K Street, N.W.  
Washington, DC 20006  
(202) 496-7500  
Attorneys for Applicant